



Inside this issue:

- Invited Talks
- Student internships
- Achievements
- Happenings at the department
- Faculty and student achievements

ABOUT THE DEPARTMENT

VISION

Global leadership and excellence in academics and research in electronics and communication engineering.

MISSION

- To deliver a competitive academic curriculum for intellectual growth and Multi-disciplinary skill acquisition.
- Promoting quality education, scientific awareness for future endeavors in higher education and career in electronics and communication industry.
- To cater to the need of promoting leadership and professionalism with human values

The Department of Electronics & Communication Engineering (ECE) at Manipal University Jaipur provides best learning environment for imparting scientific knowledge to develop practical design engineers. Keeping pace with latest technological trends and expectations of society, the department has adapted itself with continuous involvement of its stakeholders. The faculty at ECE always motivate and nurture students' inclination towards research and development with access to sophisticated PCB prototyping machine and latest software for publications in conferences and Journals of repute. The department offers 2 Undergraduate programs in Electronics and Communication Engineering and Electronics Engineering (VLSI Design & Technology); 1 Postgraduate program in VLSI and Embedded System Design and Doctorate program. The well-equipped laboratories and resourceful library at MUJ constitute the best info-oriented junction for the students.

The flexible curriculum at MUJ offers a diversified course bucket to pursue students' interest in communication systems, semiconductor and optical devices, electronic circuits, together with assembly languages, programming languages, and embedded systems, thereby opening gates to interdisciplinary growth with a wide choice of career options to pursue their interest. The ECE graduates have been spread successfully in software as well as hardware industry.

INVITED TALKS

MANIPAL UNIVERSITY JAIPUR

Professional Development Program
on
AI-Driven VLSI Design and Optimization

10 JULY 2025
4:00 PM (IST)

EXPERT:
DR SHASHI KANT SHARMA
ASSISTANT PROFESSOR
IIIT RANCHI

SCAN TO REGISTER

SCAN TO PAY REGISTRATION FEES (INR 1000/-)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
MANIPAL UNIVERSITY JAIPUR

MANIPAL UNIVERSITY JAIPUR

Professional Development Program
on
AI-Driven VLSI Design and Optimization

11 JULY 2025
11:00 AM (IST)

EXPERT:
CHETAN ARVIND PATIL
SENIOR STAFF PRODUCT
ENGINEER
INDIE, USA

SCAN TO REGISTER

SCAN TO PAY REGISTRATION FEES (INR 1000/-)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
MANIPAL UNIVERSITY JAIPUR

MANIPAL UNIVERSITY JAIPUR **VLSI SOCIETY OF INDIA**

Webinar
on
Challenges and Opportunities in applying AI in VLSI design

10 JULY 2025
11:00 AM (IST)

EXPERT:
ARPAN SIRCAR
SENIOR PRINCIPAL ENGINEER
INTEL

SCAN TO JOIN (GOOGLE MEET)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
MANIPAL UNIVERSITY JAIPUR

MANIPAL UNIVERSITY JAIPUR
(University under Section 2(f) of the UGC Act)

INDUSTRY EXPERT TALK
Architecture of MCS-51 microcontroller

DATE
28.08.2025

TIME
04:30 PM - 06:00 PM

LOCATION
RNo. 117D, 1AB, MUJ

ORGANIZER:
DR. SHILPI BIRLA

CONVENER:
DR. ROHIT MATHUR
+91-8527118621

Speaker:
Dr. Jignesh Patoliya
Senior Design Verification
Engineer at eInfochips

MANIPAL UNIVERSITY JAIPUR

CURRICULUM-BASED INDUSTRY LECTURE
DESIGN FOR TESTABILITY (ECE3143/VDT3143)

22 August 2025
10 AM onwards

RESOURCE PERSON
Mr. Sandeep Bansal
Board Design and Package
Principal Engineer
Renesas Electronics
Noida

Scan to join

Department of Electronics & Communication Engineering, School of Engineering

MANIPAL UNIVERSITY JAIPUR

CURRICULUM BASED INDUSTRY LECTURE

TITLE OF TALK:
MODERN APPLICATIONS & CASE STUDIES (MOBILE COMMUNICATION AND NETWORKS)

SPEAKER:
MR KAMAL VIJ
RADIO PLANNING MANAGER,
ZAIN, SAUDI ARABIA

29 AUGUST 2025
11:30 AM IST

SCAN TO JOIN:

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

INDUSTRY LECTURES

Department of ECE, continued its practice of organizing curriculum-based industry lectures for the students. These curriculum -based industry lectures are important for engineering students because they help bridge the gap between theory and practice and provide students with valuable real-world experience. Students can learn from the experiences and practical knowledge of industry leaders and with the connect they can get career guidance and mentorship from industry people for enhanced employability and career advancement prospects. The following experts taught students and guided them:

Mr Ankur Sangal, HCL Technologies, Noida

Dr Jignesh Patoliya, eInfochips, Ahmedabad

Mr Sandeep Bansal, Renesas, Noida

Mr Kamal Vij, Nokia, Saudi Arabia

Dr. Gaurav Kumar Sharma, Intel India pvt Ltd

STUDENT INTERNSHIPS



Students from VII semester, department of ECE bagged long-term internships

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

KANISHKA GOYAL
B.Tech. ECE
Batch: 2022-2026

For getting internship in

d-Matrix
www.d-matrix.ai

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

G.SAI NAGA JEEVANI
B.Tech. ECE
Batch: 2022-2026

For getting internship in

d-Matrix
www.d-matrix.ai

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

Soumil Hallan
B.Tech. ECE
Batch: 2022-2026

For getting long-term
internship at

DRDO, Dehradun

Students from V semester, department of ECE completed their internships

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

NIYATI GUPTA
B.Tech. ECE
Batch: 2023-2027

For completing internship
from

**The Institute of Photonic Sciences
(ICFO, Spain)**

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

ANMOL GOEL
B.Tech. ECE
Batch: 2023-2027

For completing internship
from

ECU WORLDWIDE

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

DEEPA NSHU DAS
B.Tech. ECE
Batch: 2023-2027

For completing internship
from

**Hachidori
Robotics PVT LTD**

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

Komal Saini
B.Tech. ECE
Batch: 2023-2027

For completing internship
from

BITS Pilani

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

Shreya Menon
B.Tech. ECE
Batch: 2023-2027

For getting internship in

IISER BHOPAL

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

Ankit Saini
B.Tech. ECE
Batch: 2023-2027

For completing internship
from

BITS Pilani

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

PRIYANSHU KUMAR SINGH
B.Tech. ECE
Batch: 2023-2027

For completing internship
from

Zidio Development

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

Khushi Rai
B.Tech. ECE
Batch: 2023-2027

For getting internship in

IISER BHOPAL

MANIPAL UNIVERSITY
JAIPUR
University under Section 3 of the UGC Act

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Congratulates

Jhanvi Choukse
B.Tech. ECE
Batch: 2023-2027

For getting internship in

IISER BHOPAL

Editorial Team

Dr Neha Singh. Dr Ankur Saharia

Ms Samridhi Jha (2429030143), Ms Aarushi Kumar (2429030101)

ACHIEVEMENTS



RESEARCH LAB INAUGURATED



The Laboratory for Energy and Advanced Functional (LEAF) Materials was inaugurated at Manipal University Jaipur (MUJ) on October 6, 2025, by Prof. Ashutosh Sharma, President of the Indian National Science Academy and former Secretary, DST, Government of India. Graced by Prof. N. N. Sharma, President, MUJ, and Prof. Kuldip Singh Sangwan, Dean, Faculty of Science, Technology & Architecture, the event marked a major milestone in research on green energy, sustainable materials, and catalysis. The lab aims to drive innovation in next-generation materials for fuel cells, CO₂ conversion, and semiconductor technologies, led by Dr. Dinesh Bhalothia, Dr. Amisha Beniwal, and Dr. Ashima Bagaria.

ORIENTATION PROGRAM



MUJ welcomed its newest batch with energy, enthusiasm, and unforgettable moments during the Orientation Program 2025. From inspiring sessions to campus explorations, it's the start of something amazing.

MUJ Excellence Awardees 2025



Prof. Manish Tiwari



Mr. Ashish Vijay



Dr. Abhishek Shrivastava



Dr. Suddhendhu Das Mahapatra



Prof. Shilpi Birla



Prof. Amit Rathie



Dr. Dinesh Yadav



Dr. Prashant Povel Diwedi

HAPPENINGS AT THE DEPARTMENT

MEET YOUR LEADERS: Perspectives from Space & Defence



Department of ECE, in collaboration with IEEE MTT-S, IEEE AESS, and COSMOS, proudly welcomed **Dr. Goutam Chattopadhyay (NASA JPL)**, **Dr. Chinmoy Saha (IIST)**, and **Dr. Ashutosh Kedar (DRDO)** on **12 September 2025**. The leaders shared inspiring insights on space exploration, defence technologies, and future opportunities for young innovators. The engaging talks and panel discussions left students motivated to pursue research, innovation, and global collaborations—echoing MUJ's vision of nurturing future-ready leaders.

DISTINGUISHED VISITS



We were honoured to welcome international delegation including **Prof. Manish Pokharel, Dean of the School of Engineering**, and **Prof. Manoj Humagain, Dean of the School of Medical Sciences**, from **Kathmandu University, Dhulikhel, Nepal**, at our **MUJ-NLO+ Research Laboratory** (funded by DRDO), Department of Electronics and Communication Engineering (ECE) on **4 August 2025**. **Dr. Arjun Kumar (DRDS), Additional Director & Scientist 'F', Directorate Technology Development Fund (DTDF), DRDO, Ministry of Defence**, visited the **MUJ-NLO+ Research Laboratory** on **8 September 2025**. We also hosted **Prof. Avinash Chandra Pandey, Director of the Inter-University Accelerator Centre (IUAC), New Delhi**, at **MUJ-NLO+ lab** on **11 September 2025**.

Student Chapters at ECE, MUJ



Manipal University Jaipur
Student Chapter



The Optica Student Chapter MUJ and SPIE MUJ hosted a PCB Design workshop, where participants built an astable multivibrator circuit using IC 555 in EasyEDA.



HAPPENINGS AT THE DEPARTMENT

Faculty and student visit to SEMICON India-2025, New Delhi

Department faculty members, Professor Shilpi Birla, Dr Tejpal and Mr Hemendra Talesra participated in the national Semicon India 2025 at Yashobhoomi, New Delhi with Professor R. K. Gupta, Associate Dean, School of Engineering and UG/PG students. The event brought together global leaders, innovators, and researchers to discuss the latest advancements in chip design, manufacturing, materials, and future technologies.



Student exchange under IAESTE

3 students registered in the department of ECE under the student exchange program by IAESTE for 2025-26 session. **Mohammad Golestanbagh** from Iran completed his internship under the guidance of **Dr Deepika Bansal** during July-Aug 2025. **Amani Kardous** from Tunisia and **Felipe Fernández-Arche Pineda** from Spain completed their internship under the guidance of **Dr Tarun Kumar Dubey** during July-Aug 2025.

Faculty Members Welcomed

Department of ECE welcomes **new faculty members** to the family :

Dr Arun Kishore Johar
Dr D.V. Prashant
Dr Kamal Kishore Upadhyay

Value added courses offered to UG students

MANIPAL UNIVERSITY JAIPUR University under Section 22 of the UGC Act VLSI SoC Design using Verilog HDL (Value Added Course) Duration of course offered 30 hours Organized by Maven Silicon Centre of excellence in VLSI Department of Electronics & Communication Engineering Manipal University Jaipur	About the course This course encompasses key modules such as VLSI introduction, SoC Design, ASIC vs FPGA, VLSI Design Flow, and Verilog HDL essentials. The students will learn data types, Verilog operators, system tasks & functions, and engage in hands-on Verilog Labs. This will elevate their understanding of structured procedures to master the art of designing complex systems for seamless integration and efficiency. Aim of the course This course is tailored to provide the students with a robust foundation in VLSI SoC Design using Verilog HDL. Learning outcomes of the course - Understand the VLSI SoC design steps - Explain modeling of combinational and sequential digital systems for acquiring skills in the domain of Digital Systems. - Gain proficiency in using Verilog HDL. Detailed Content of the course - Unit 1: Introduction to SoC Design - Unit 2: Introduction to Verilog HDL - Unit 3: Verilog datatypes and operators - Unit 4: System Tasks and Functions - Unit 5: Assignments Eligibility Criteria All Undergraduate scholars of the Electronics and Communication Engineering at Manipal University Jaipur. Fees No fees for students of Manipal University Jaipur.	Resource Person's Resource person from the Department of Electronics & Communication Engineering supports the delivery of the course. The program is conducted online by Maven Silicon. Duration of the course The course will be conducted for a total of 4 weeks (30 hrs). The course shall begin from 2 July 2025 and conclude by 1 August 2025. Intake Selection is purely based on a first-come-first-serve basis. Admission procedure - Interested participants can avail admission to the course by registering at https://forms.gle/taufuVWVYU2G8 - The admission will be closed after the completion of required number of student in a batch on first come first serve basis by the organizer Assessment The assessment will be based on completion of assignments and performance in the test conducted at the end of the course. Contact Dr Neha Singh Assistant Professor, Department of Electronics & Communication Engineering, Manipal University Jaipur neha.singh@jaipur.manipal.edu
--	---	--

MANIPAL UNIVERSITY JAIPUR University under Section 22 of the UGC Act "Fabrication of Semiconductor Device" (Value Added Course) Organized by Department of Electronics and Communication Engineering Manipal University Jaipur About the course The course is intended to provide an understanding of current fabrication practices used in the semiconductor industry, along with the challenges and opportunities in device fabrication. The purpose of this programme is to train the manpower aligned for the India Semiconductor Mission (ISM).	Aim of the course Aim: The aim of this course is to provide students a lab extends beyond classroom concepts to real-world research and Hand-on experience on fabrication of semiconductor devices. Learning outcomes of the course - Hand-on experience on cleaning and Drying of Silicon Wafer. - Hand-on experience on oxidation process. - Hand-on experience on Photolithography process. - Hand-on experience on Diffusion process. - Hand-on experience on Ion Implantation process. - Hand-on experience on semiconductor device testing method. Detailed Content of the course This course deals with the various steps involved in the fabrication of semiconductor device, starting with silicon wafers the course contents are divided into four steps as per the plan below. 1: Introduction and overview of semiconductor device fabrication. 2: Fabrication operations: Oxidation, doping, and lithography. 3: Fabrication processes: etching and growth. Process evaluation. 4: Process yield, clean room design, and IC logic and packaging. Eligibility Criteria All Undergraduate, postgraduate and PhD scholars of the Manipal University Jaipur. Fees No fees for students of Manipal University Jaipur.	Resource Person's The programme is conducted by Dr. Prashant Poovali and Dr. Rajendra Kasana, Department of Electronics and Communication Engineering. The sessions are conducted in semiconductor fabrication lab and using relevant visual aids, chalk talk methods and other classroom teaching learning methods. Duration of the course The course will be conducted for a total of 45 days (30 hrs.) beginning from 8 August 2025. Intake Selection is purely based on a first-come-first-serve basis, with an intake of 30 participants per batch. Admission procedure - Interested participants can avail admission to the course through office of the Department of Electronics and Communication Engineering on a first-come-first-serve basis. - The admission will be closed after completion of required number of student in a batch. Assessment Assessment will be based on completion of assignments and performance in the test conducted at the end of the course. - At least 40 percentage marks in the test is compulsory to avail the Course Completion certificate. Special Instructions: - Regular attendance in teaching sessions is mandatory. Contact Dr. Prashant Poovali Assistant Professor (Selection Scale), Department of ECE, Manipal University Jaipur prashant.poovali@jaipur.manipal.edu
--	---	--

02 Value added course for 30 hours and 45 hours are offered by Department of ECE with Maven Silicon for the UG students. The VLSI SoC Design with Verilog HDL will help to elevate students' understanding of structured procedures to master the art of designing complex systems for seamless integration and efficiency. The device fabrication course aims to provide students a lab extends beyond classroom concepts to real-world research and Hand-on experience on fabrication of semiconductor devices.

FACULTY AND STUDENT ACHIEVEMENTS

Ph. D. Awarded



Annu Tiwari and Gaurav Soni were awarded Ph. D. under the guidance of Dr.Dinesh yadav.



Soumil Hallan successfully completed all three levels of the India Semiconductor Workforce Development Program (ISWDP) an initiative by MSDLab @ IISc, Samsung Semiconductor Research India and Synopsys Inc.

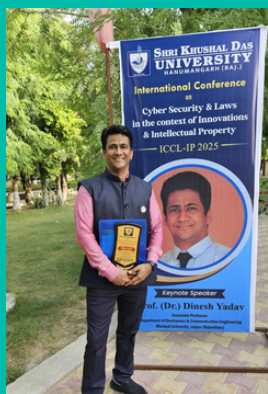


Agrima Joshi stood 2nd at INDGenius volleyball league organized by INDGenius at MUJ from 8th to 15th September 2025



The proceedings book of the 1st International Conference on Sensors and Microsystems (ICSM-2024) was published in Sept 2025. The book was edited by Prof. Manish Tiwari and Dr. Neha Singh from MUJ.

Utsav Sarkar of B.Tech ECE 2nd Year participated in the Robotic Competition, WRC Technoxian 2025 held between 30th August to 2nd September in Noida.



Dr Dinesh Yadav delivered a talk on Innovations and Intellectual Property Rights in International conference at Shri Khushal Das University, Hanumangarh, Rajasthan



Dr Dinesh Yadav delivered a talk during 3-Days Workshop on "Futuristic Communication Techniques with AI-ML", organized by the Department of IoT, Poornima Institute of Engineering & Technology